

HSMC DVI 1080P EDID Update

This update re-flashes the EDID data in the EEPROM on the HSMC DVI board. The updated EDID data contains the required information for “1080P60 Reduced Blanking 5%” operation.

The following steps should be performed to update correctly.

1. Install the BITEC HSMC board on J8 (HSMA) of the CIII Dev Kit motherboard or the single HSMC port on the CIII Eval Kit.
2. Open the Quartus II programmer and select the update sof file appropriate for the platform.
3. Click Start in the programmer to download the sof.
4. When download is complete the sof will re-flash the EDID EEPROM.
5. When the update is complete USER LED1 will flash.
6. Power off the CIII VDK.

Note: No DVI monitors should be connected to the RX port of the HSMC DVI board during update.

After re-flash, the EDID will report the following X11 Modelike

```
Modeline "1920x1080" 134 1920 1940 2000 2192 1080 1084 1089 1125 +hsync +vsync
```

The full characteristics for the Modelline can be seen in the panel right.

Timings			
Pixel Clk:	134	Interlaced	<input type="checkbox"/>
H Active Pxl:	1920	V Active Lines:	1080
H Blank:	272	V Blank:	45
H Sync Offset:	20	V Sync Offset:	4
H Sync Width:	60	V Sync Width:	5
H Image Size:	160	V Image Size:	90
H Border:	0	V Border:	0